- 10 -   NE-1148

FIG. 13

START

DSV1 = DSV2 ← 0
POL1 = POL2 ← +1
DSV CTRL BIT ← NULL   201

CONVERT 4 DATA BITS
FROM SHIFT REGISTER
TO 3 CHANNEL BITS   601

i = MSB   205

READ BIT FROM
POSITION "i"   206

i = DSV CTRL BIT ?   Yes   207
No

i = "1" OR "0" ?   0   208
1

POL2 ← POL2 X (-1)   209

POL1 ← POL1 X (-1)   210

DSV1 ← DSV1 + POL1
DSV2 ← DSV2 + POL2   211

i = i - 1   213

i = LSB ?   No   212
Yes

END OF DATA ?   No   Yes   214

CONVERTED TO "010" ?   No   222
Yes

CURRENT STATE = S1 ?   No   602
Yes   No

NEXT DSV CTRL BIT ← CENTER BIT POS.   224

DSV CTRL BIT = NULL ?   Yes   225
No

|DSV1| ≥ |DSV2| ?   ≥   226
<

DSV CTRL BIT ← "1"
POL2 ← POL1
DSV2 ← DSV1   227

DSV CTRL BIT ← "0"
POL1 ← POL2
DSV1 ← DSV2   228

DSV CTRL BIT ← NEXT DSV CTRL BIT   229

DSV CTRL BIT = NULL ?   Yes   231
No

|DSV1| ≥ |DSV2| ?   ≥   232
<

DSV CTRL BIT ← "1"
POL2 ← POL1
DSV2 ← DSV1   233

DSV CTRL BIT ← "0"
POL1 ← POL2
DSV1 ← DSV2   234

STOP

NEXT STATE = S0